


PATENT NUMBER

U.S. UTILITY Patent Application

	O.I.P.E. SCANNED <u>AA³</u> Q.A. <u>UA</u>	PATENT DATE
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APPLICATION NO. 09/689532	CONT/PRIOR	CLASS 325 324	SUBCLASS 4-1	ART UNIT 2819 2812	EXAMINER WILTMORE
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APPLICANTS

TITLE

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Circuit for generating silicon ID for PLDS

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div style="border-bottom: 1px solid black; margin-bottom: 5px;"></div> <div style="display: flex; justify-content: space-between; font-size: small;"> (Assistant Examiner) (Date) </div>			NOTICE OF ALLOWANCE MAILED	
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